

METHOD AND APPARATUS FOR DETECTING ROBBED
BIT LOCATION IN PCM MODEMS AND THE LIKE

Abstract of the Disclosure

A method and apparatus for detecting the location of
5 robbed bits by a digital communications network. A complete
echo cancellation circuit also is disclosed employing the
robbed bit detection method and apparatus in which the
coefficients of the echo cancellation FIR filter are trained
simultaneously during the detection of the position of the
10 robbed bit or bits.